

SPECIFICATIONS FOR LCD MODULE

Version: A1

CUSTOMER	
MODEL	SC1602013-V01
CUSTOMER APPROVED	

APPROVED BY	CHECKED BY	ORGANIZED BY
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Specification Revision History

Version: A1

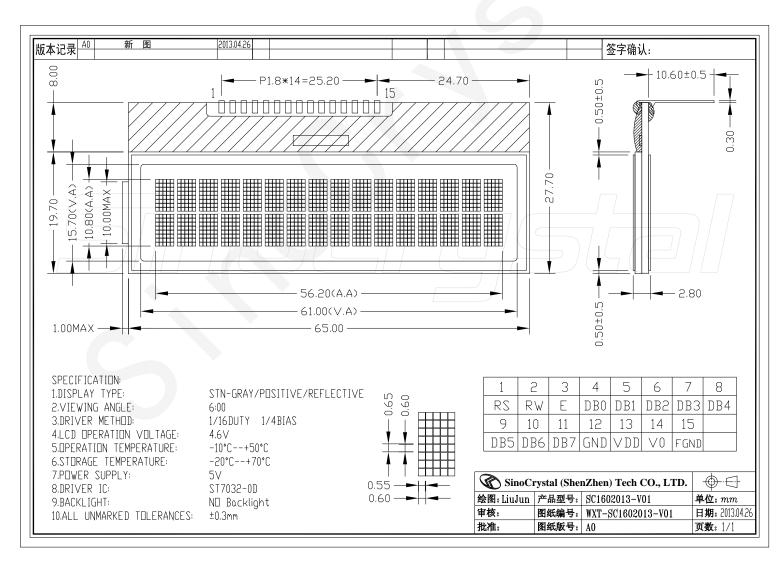
Version	Content	Date				
A0	First Issue	16-May-2012				
A1	Change Viewing angle range Test Condition	16-Jul-2013				
		0				
*						



1. PHYSICAL DATA

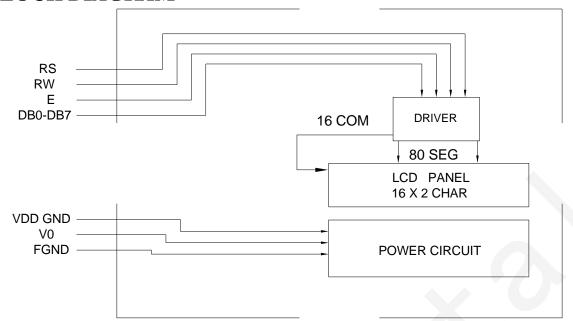
ITEM	STANDARD VALUE	UNIT
NUMBER OF CHARACTER	16×2	Mm
MODULE DIMENSION	65.0×27.7×10.6(MAX)	Mm
VIEWING AREA	61.0×15.7	Mm
DOT SIZE	0.55×0.65	Mm
DOT PITCH	0.60×0.65	Mm
LCD TYPE	STN/GRAY/POSITIVE/REFLECTIVE	
DUTY	1/16	
VIEWING DIRECTION	6:00	o'clock
BACK LIGHT TYPE	NO BACKLIGHT	
BACK LIGHT COLOR	NO BACKLION I	
APPROX. WEIGHT	TBD	G

2. EXTERNAL DIMENSIONS





3. BLOCK DIAGRAM



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4. INTERFACE PIN CONNECTIONS

Pin No.	Symbol	Level	Description
			Register selection input
1	RS	H/L	H: Indicate that DB0 to DB7 are display data.
			L: Indicate that DB0 to DB7 are control data
			Read or Write signal
2	RW	H/L	0 : Write
			1 : Read
3	Е	H/L	Enable signal
4-11	DB0-DB7	H/L	8-bit bi-directional data bus
12	GND		Gound
13	VDD		Supply voltage for logic
14	V0		Supply voltage for LCD
15	FGND		Frame Ground



5. ABSOLUTE MAXIMUM RATINGS

(1) Electrical Absolute Ratings

Item	Symbol	Min.	Max.	Unit	Note
Power Supply for Logic	VDD	-0.3	6.0	Volt	Note 1
Power Supply for LCD	V_{LCD}	-0.3	7.0	Volt	
Input Voltage	$V_{\rm I}$	-0.3	VDD	Volt	
Current for LED backlight	I_{LED}	-	-	mA	No Backlight

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Note 1: Operator should be grounded during handling LCM

(2) Environmental Absolute Maximum Ratings

	No	rmal T	empera	ture	Wide Temperature					
Item	Ope	rating	Sto	rage	Opei	rating	Storage			
	Min.	Max,	Min.	Max,	Min.	Max,	Min.	Max,		
Ambient Temperature	0℃	+50°C	-10°C	+60°C	-10°C	+50℃	-20°C	+70℃		
Humidity(without condensation)	Note 2,4		Not	e 3,5	Not	e 4,5	Note 4,6			

Note 2 Ta $\leq 50^{\circ}$ C: 80% RH max

Ta>50°C: Absolute humidity must be lower than the humidity of 85%RH at 50°C

Note 3 Ta at -20°C will be <48hrs at 70°C will be <120hrs when humidity is higher than 75%.

Note 4 Background color changes slightly depending on ambient temperature. This phenomenon is reversible.

Note 5 $Ta \le 70^{\circ}C: 75RH \text{ max}$

Ta>70°C: absolute humidity must be lower than the humidity of 75%RH at 70°C

Note 6 Ta at -20°C will be <48hrs, at 80°C will be <120hrs when humidity is higher than 75%.

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6. ELECTRICAL CHARACTERISTICS

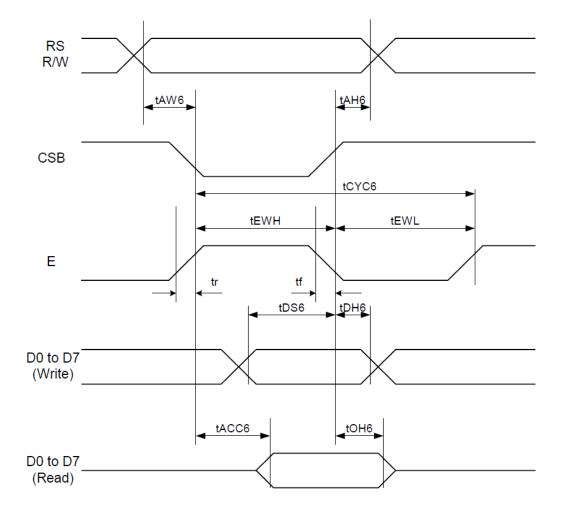
DC Characteristics

(GND=0V,VDD=5V, Ta=25℃)

Item	Symbol	Test Condition	Min.	Typ.	Max.	Unit		
Power Supply for Logic	VDD		4.5	-	5.5	Volt		
Innut Valtage	V_{IH}		2.7	-	VDD	Volt		
Input Voltage	$V_{\rm IL}$		-0.3	-	0.8	Volt		
Ontone Valley	V _{OH}	I _{OUT} =-1mA	3.8	-	VDD	Volt		
Output Voltage	V _{OL}	I _{OUT} =1mA	-	-	0.8	Volt		
		$T_a = 0$ °C	-	-	-			
LCM Recommend LCD Module Driving Voltage	V_{LCD}	$T_a=25^{\circ}C$	-	4.6		Volt		
Diving voluge		$T_a=50$ °C	-	-	-			
Power Supply Current for LCM	I _{DD} (BL OFF)	T _a =25℃	-		2	mA		
rower suppry surrent for Bern	I _{DD} (BL ON)		No					
Power Supply for LED Backlight	V _{BLA} - V _{BLK}	Ta=25°C	NO	V				



AC Characteristics



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 $(Ta = 25^{\circ}C)$

ltem	Signal	Symbol	Condition		7 to 4.5V ting	VDD=4.	units		
item	Signal	Symbol	Condition	Min.	Max.	Min.	Max.	Onits	
Address hold time	RS	t AH6	_	20	-	20	-	ns	
Address setup time	RS	taw6		20	-	20	-	113	
System cycle time	RS	tcyc6	_	400	-	280	-	ns	
Data setup time	D0 to D7	t _{DS6}		100	-	80	-		
Data hold time	D0 to D7	t DH6		40	-	20	-	ns	
Access time	D0 to D7	t ACC6	0. 400 5	-	500	-	400		
Output disable time	D0 to D7	t 0H6	CL = 100 pF	300	-	150	-	ns	
Enable Rise/Fall time	E	tr,tf	_	-	20	-	20	ns	
Enable H pulse time	Е	t EWH	_	200	-	120	-	ns	
Enable L pulse time	Е	tewL	_	150	-	130	-	ns	

Note: All timing is specified using 20% and 80% of VDD as the reference.

Note: CSB have been connected to GND

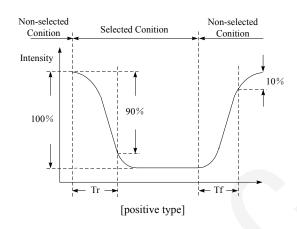


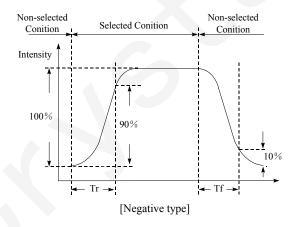
7. ELECTRO-OPTICAL CHARACTERISTICS

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Item	Symbol	Condition	Min.	Typ.	Max.	Unit	note
	$\theta_f(12 \text{ o'clock})$			40			
Viewing angle range	θ_b (6 o'clock)	When $C_n \ge 1.1$		45		Degree	Note 2
	θ_l (9 o'clock)	When $Cr \ge 1.1$		30			Note 3 Note 4
	θ_r (3 o'clock)			30			
Rise Time	T _r			112		C	Note 1
Fall Time	T_{f}	V_0 -GND=4.6V Ta=25°C		250		mS	Note 1
Contrast	Cr	14 23 0		5. 4			

[Note 1] Definition of Response Time (Tr, Tf)

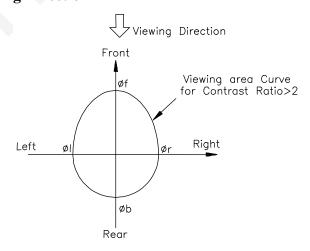




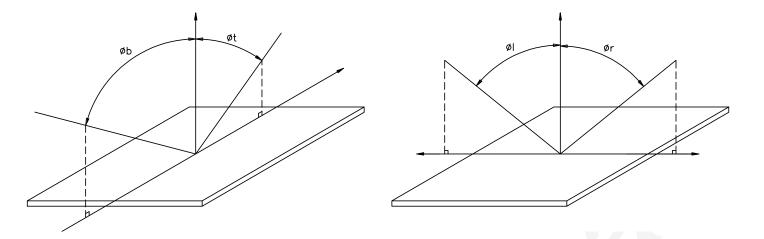
Conditions:

Operating Voltage : Vop Frame Frequency : 64 Hz Viewing Angle(θ , φ): 0°, 0° Driving Wave form : 1/N duty, 1/a bias

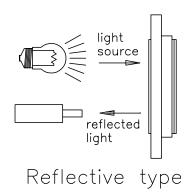
[Note 2] Definition of Viewing Direction

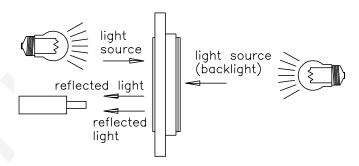


[Note 3] Definition of viewing angle



[Note 4] Description of Measuring Equipment





Transflective type



8. OPERATING PRINCIPLES & METHODS

Command Table

(ST7032 support 2 command mode, SC1602013 is Normal mode, EXT have been connected to VDD)

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> instruction table at "Normal mode"

(When "EXT" option pin connect to VDD, the instruction set follow below table)

(WITEH LAT		1011							Idoti	011 0	Et follow below table)	lı lı	nstructio	n
			lr	ารtr	ucti	on	Cod	de			D	ll .	cution T	
Instruction	RS	R/W	DP7	DB6	DP5	DD4	DB2	DB2	DB1	DBA	Description	osc=	OSC=	OSC=
	KS	IK/ VV	ופט	DBO	DB3	DB4	DB3	DBZ	ופט	DBU		380KHz	540kHz	700KHz
Clear Display	0	0	0	0	0	0	0	0	0	1	Write "20H" to DDRAM. and set	1.08	0.76	0.59
Cicai Display					Ů	Ů	Ů		Ů	'	DDRAM address to "00H" from AC	ms	ms	ms
											Set DDRAM address to "00H" from			
Return Home	0	0	0	0	0	0	0	0	1	x	AC and return cursor to its original	1.08	0.76	0.59
			•					•			position if shifted. The contents of	ms	ms	ms
											DDRAM are not changed.			
											Sets cursor move direction and			
Entry Mode	0	0	0	0	0	0	0	1	I/D	s	specifies display shift. These	26.3 us	18.5 us	14.3 us
Set											operations are performed during			
											data write and read.			
Display								_		_	D=1:entire display on	00.0	40.5	440
ON/OFF	0	0	0	0	0	0	1	D	С	В	C=1:cursor on	26.3 us	18.5 us	14.3 us
											B=1:cursor position on			
Cureer or											S/C and R/L:			
Cursor or	0	0	0	0	0	1	S/C	R/L	х	х	Set cursor moving and display shift	26.3 us	18.5 us	14.3 us
Display Shift											control bit, and the direction, without			
											changing DDRAM data.			
Function Set	0	0	0	0	1	DL	N	x	x	x	DL: interface data is 8/4 bits	26.3 us	18.5 us	14.3 us
											N: number of line is 2/1			
Set CGRAM					۸.05		400			۸.00	Set CGRAM address in address	26.2	18.5 us	442.00
Set CGRAIN	0	0	0	1	AC5	AC4	AC3	AC2	ACT	ACU	counter	26.3 us	16.5 us	14.3 us
Set DDRAM											Set DDRAM address in address			
address	0	0	1	AC6	AC5	AC4	AC3	AC2	AC1	AC0	counter	26.3 us	18.5 us	14.3 us
											Whether during internal operation or			
Read Busy											not can be known by reading BF.			
flag and	0	1	BF	AC6	AC5	AC4	AC3	AC2	AC1	AC0	The contents of address counter	0	0	0
address											can also be read.			
Write data											Write data into internal RAM			
to RAM	1	0	D7	D6	D5	D4	D3	D2	D1	D0	(DDRAM/CGRAM)	26.3 us	18.5 us	14.3 us
Read data											Read data from internal RAM			
from RAM	1	1	D7	D6	D5	D4	D3	D2	D1	D0	(DDRAM/CGRAM)	26.3 us	18.5 us	14.3 us

Note:

Be sure the ST7032 is not in the busy state (BF = 0) before sending an instruction from the MPU to the ST7032. If an instruction is sent without checking the busy flag, the time between the first instruction and next instruction will take much longer than the instruction time itself. Refer to Instruction Table for the list of each instruction execution time.

NOTE: For more detail information, please refer to the ST7032's specification.



9. Display Data RAM (DDRAM)

Display data RAM (DDRAM) stores display data represented in 8-bit character codes. Its extended capacity is 80 x 8 bits, or 80 characters. The area in display data RAM (DDRAM) that is not used for display can be used as general data RAM. See Figure 7 for the relationships between DDRAM addresses and positions on the liquid crystal display.

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The DDRAM address (ADD) is set in the address counter (AC)as hexadecimal.

Display Position	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
DDRAM	00	01	02	03	04	05	06	07	80	09	0A	0B	0C	0D	0E	0F
Address	40	41	42	43	44	45	46	47	48	49	4A	4B	4C	4D	4E	4F
For Shift	01	02	03	04	05	06	07	80	09	0A	0B	0C	0D	0E	0F	10
Left	41	42	43	44	45	46	47	48	49	4A	4B	4C	4D	4E	4F	50
For Shift	27	00	01	02	03	04	05	06	07	80	09	0A	0B	0C	0D	0E
Right	67	40	41	42	43	44	45	46	47	48	49	4A	4B	4C	4D	4E

NOTE: For more detail information, please refer to the ST7032's specification.



Product Num	ber Of	PR1	OPR2	Support Character			
ST7032-00)	1	0	English/Japan/European			

ST7032-0D (ITO option OPR1=1, OPR2=0)

67-64 63-60	0000	0001	0010	0011	0100	0101	0110	0111	1000	1001	1010	1011	1100	1101	1110	1111
0000																
0001																
0010	Replaced															
0011	ed By C															
0100	GRAM															
0101	Pattern															
0110																
0111																
1000																
1001																
1010																
1011																
1100																
1101																
1110																
1111																



10.RELIABILITY

	Environmental Test							
No.	Test Item	Content of Test	Test Condition	Applicable Standard				
1	High temperature storage	Endurance test applying the high storage temperature for a long time.	70 °C 200 hrs					
2	Low temperature storage	Endurance test applying the low storage temperature for a long time.	-20 °C 200 hrs					
3	High temperature operation	Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time.	50 °C 200 hrs					
4	Low temperature operation	Endurance test applying the electric stress under low temperature for a long time.	-10 °C 200 hrs					
5	High temperature / Humidity storage	Endurance test applying the high temperature and high humidity storage for a long time.	60 °C , 90 %RH 96 hrs	MIL-202E-103B JIS-C5023				
6	High temperature / Humidity operation	Endurance test applying the electric stress (Voltage & Current) and temperature / humidity stress to the element for a long time.	50 °C , 90 %RH 96 hrs	MIL-202E-103B JIS-C5023				
7	Temperature cycle	Endurance test applying the low and high temperature cycle. -10°C \(\sigma \frac{25°C}{5min.} \sigma \frac{50°C}{30min} \) 1 cycle	-10°C / 50°C 10 cycles					
		Mechanical Test						
8	Vibration test	Endurance test applying the vibration during transportation and using.	$10\sim22$ Hz $\rightarrow 1.5$ mmp-p $22\sim500$ Hz $\rightarrow 1.5$ G Total 0.5hrs	MIL-202E-201A JIS-C5025 JIS-C7022-A-10				
9	Shock test	Constructional and mechanical endurance test applying the shock during transportation.	50G half sign wave 11 msedc 3 times of each direction	MIL-202E-213B				
10	Atmospheric pressure test	Endurance test applying the atmospheric pressure during transportation by air.	115 mbar 40 hrs	MIL-202E-105C				
	Others							
11	Static electricity test	Endurance test applying the electric stress to the terminal.	VS=800V , RS=1.5 kΩ CS=100 pF 10 time	MIL-883B-3015.1				

Inspection after test: Inspection after $2\sim4$ hours storage at room temperature, the sample shall be free from defects:

- 1. Air bubble in the LCD.
- 2. Sealleak
- 3. Non-display.
- 4. Missing segments.
- 5. Glass crack.
- 6. Current Idd is twice higher than initial value.



11.QUALITY GUARANTEE

LCD Module Specification

No	Item	Criteria						
		(1)round type						
		diameter mm(a*) no of defect*						
		$a \le 0.20$ neglect						
		$0.20 < a \le 0.35$ 5 max						
1	inclusions (black spot,	0.35 < a none						
1	white spot, dust)	(2)linear type						
		length mm(l) width mm(W) no. of defect						
		na W≤0.03 neglect						
		$1 \le 3$ $0.03 < W \le 0.08$ 6						
		3 < 1 0.08 < W none						
		1.scratch on protective film is permitted.						
		2. scratch on polarizer shall be as follow:						
		(1)round type						
		diameter mm(a*) no of defect						
2	scratch	$a \leq 0.15$ neglect						
		$0.15 < a \le 0.20$ 2 max						
		0.20 < a none						
		(2)linear type						
		be judged bye 1(2) linear type						
3	dent	diameter < 1.5mm						
4	hyshkla	not exceeding 0.5mm average diameter is acceptable between glass						
4	bubble	and polarizing film						
	pin hole	$(a+b)/2 \le 0.15$ mm						
5		maximum number: ignored						
3		$0.15 < (a+b)/2 \le 0.20$ mm						
		maximum number:10						
6	dot width	design width ±15%						
		$(a+b)/2 \le 0.20$ mm						
	dot defect	maximum number: ignored						
7		$0.20 < (a+b)/2 \le 0.30$ mm						
		maximum number:5						
		x=width						
	contrast irregularity(spot)	diameter spec no of defect						
		$a \le 0.50 \text{mm}$ neglect						
8		$0.50 < a \le 0.75$						
		$0.75 < a \le 1.00$						
		1.00 < a none						
9	color tone and uniformity	obvious uneven color is not permitted						



12.USING LCD MODULES

12-1. Liquid Crystal Display Modules

- LCD is composed of glass and polarizer. Pay attention to the following items when handling.
- (1) Please keep the temperature within specified range for use and storage. Polarization degradation, bubble generation or polarizer peel-off may occur with high temperature and high humidity.

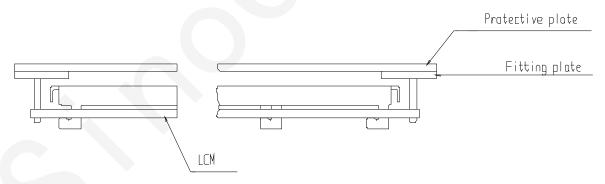
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- (2) Do not touch, push or rub the exposed polarizers with anything harder than an HB pencil lead (glass, tweezers, etc.).
- (3) N-hexane is recommended for cleaning the adhesives used to attach front/rear polarizers and reflectors made of organic substances which will be damaged by chemicals such as acetone, toluene, ethanol and isopropylalcohol.
- (4) When the display surface becomes dusty, wipe gently with absorbent cotton or other soft material like chamois soaked in petroleum benzin. Do not scrub hard to avoid damaging the display surface.
- (5) Wipe off saliva or water drops immediately, contact with water over a long period of time may cause deformation or color fading.
 - (6) Avoid contacting oil and fats.
- (7) Condensation on the surface and contact with terminals due to cold will damage, stain or dirty the polarizers. After products are tested at low temperature they must be warmed up in a container before coming is contacting with room temperature air.
 - (8) Do not put or attach anything on the display area to avoid leaving marks on.
- (9) Do not touch the display with bare hands. This will stain the display area and degradate insulation between terminals (some cosmetics are determinated to the polarizers).
- (10) As glass is fragile. It tends to become or chipped during handling especially on the edges. Please avoid dropping or jarring.

12-2.Installing LCD Modules

The hole in the printed circuit board is used to fix LCM as shown in the picture below. Attend to the following items when installing the LCM.

(1) Cover the surface with a transparent protective plate to protect the polarizer and LC cell.



(2) When assembling the LCM into other equipment, the spacer to the bit between the LCM and the fitting plate should have enough height to avoid causing stress to the module surface, refer to the individual specifications for measurements. The measurement tolerance should be ± 0.1 mm.

12-3. Precaution for Handing LCD Modules

Since LCM has been assembled and adjusted with a high degree of precision, avoid applying excessive shocks to the module or making any alterations or modifications to it.

- (1) Do not alter, modify or change the shape of the tab on the metal frame.
- (2) Do not make extra holes on the printed circuit board, modify its shape or change the positions of components to be attached.
 - (3) Do not damage or modify the pattern writing on the printed circuit board.
 - (4) Absolutely do not modify the zebra rubber strip (conductive rubber) or heat seal connector.



- (5) Except for soldering the interface, do not make any alterations or modifications with a soldering iron.
 - (6) Do not drop, bend or twist LCM.

LCM is easy to be damaged. Please note below and be careful for handling. Correct handling:





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As above picture, please handle with anti-static gloves around LCM edges.

Incorrect handling:



Please don't touch IC directly.



Please don't stack LCM.



Please don't hold the surface of panel.



Please don't stretch interface of output, such as FPC cable.



Please don't hold the surface of IC.



Please don't operate with sharp stick such as pens.



12-4. Electro-Static Discharge Control

Since this module uses a CMOS LSI, the same careful attention should be paid to electrostatic discharge as for an ordinary CMOS IC.

(1) Make certain that you are grounded when handing LCM.

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- (2) Before remove LCM from its packing case or incorporating it into a set, be sure the module and your body have the same electric potential.
- (3) When soldering the terminal of LCM, make certain the AC power source for the soldering iron does not leak.
- (4) When using an electric screwdriver to attach LCM, the screwdriver should be of ground potentiality to minimize as much as possible any transmission of electromagnetic waves produced sparks coming from the commutator of the motor.
- (5) As far as possible make the electric potential of your work clothes and that of the work bench the ground potential.
- (6) To reduce the generation of static electricity be careful that the air in the work is not too dried. A relative humidity of 50%-60% is recommended.

12-5. Precaution for soldering to the LCM

- (1) Observe the following when soldering lead wire, connector cable and etc. to the LCM.
 - Soldering iron temperature : $280^{\circ}\text{C} \pm 10^{\circ}\text{C}$.
 - Soldering time: 3-4 sec.
 - Solder : eutectic solder.

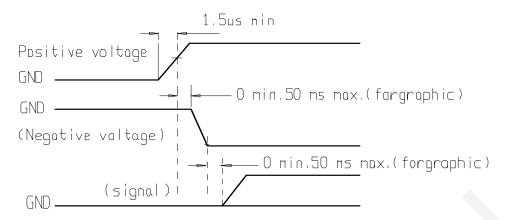
If soldering flux is used, be sure to remove any remaining flux after finishing to soldering operation. (This does not apply in the case of a non-halogen type of flux.) It is recommended that you protect the LCD surface with a cover during soldering to prevent any damage dur to flux spatters.

- (2) When soldering the electroluminescent panel and PC board, the panel and board should not be detached more than three times. This maximum number is determined by the temperature and time conditions mentioned above, though there may be some variance depending on the temperature of the soldering iron.
- (3) When remove the electoluminescent panel from the PC board, be sure the solder has completely melted, the soldered pad on the PC board could be damaged.

12-6.Precautions for Operation

- (1) Viewing angle varies with the change of liquid crystal driving voltage (VO). Adjust VO to show the best contrast.
 - (2) Driving the LCD in the voltage above the limit shortens its life.
- (3) Response time is greatly delayed at temperature below the operating temperature range. However, this does not mean the LCD will be out of the order. It will recover when it returns to the specified temperature range.
- (4) If the display area is pushed hard during operation, the display will become abnormal. However, it will return to normal if it is turned off and then back on.
- (5) Condensation on terminals can cause an electrochemical reaction disrupting the terminal circuit. Therefore, it must be used under the relative condition of 40°C, 50% RH.
 - (6) When turning the power on, input each signal after the positive/negative voltage becomes stable.





12-7. Storage

When storing LCDs as spares for some years, the following precaution are necessary.

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- (1) Store them in a sealed polyethylene bag. If properly sealed, there is no need for dessicant.
- (2) Store them in a dark place. Do not expose to sunlight or fluorescent light, keep the temperature between 0°C and 35°C.
- (3) The polarizer surface should not come in contact with any other objects. (We advise you to store them in the container in which they were shipped.)
 - (4) Environmental conditions:
 - Do not leave them for more than 168hrs. at 60°C.
 - Should not be left for more than 48hrs, at -20°C.

12-8. Safety

- (1) It is recommended to crush damaged or unnecessary LCDs into pieces and wash them off with solvents such as acetone and ethanol, which should later be burned.
- (2) If any liquid leakes out of a damaged glass cell and comes in contact with the hands, wash off thoroughly with soap and water.

12-9. Return LCM under warranty

No warranty can be granted if the precautions stated above have been disregarded. The typical examples of violations are :

- Broken LCD glass.
- PCB eyelet's damaged or modified.
- PCB conductors damaged.
- Circuit modified in any way, including addition of components.
- PCB tampered with by grinding, engraving or painting varnish.
- soldering to or modifying the bezel in any manner.

Module repairs will be invoiced to the customer upon mutual agreement. Modules must be returned with sufficient description of the failures or defects. Any connectors or cable installed by the customer must be removed completely without damaging the PCB eyelet's, conductors and terminals.