SPECIFICATIONS FOR LCD MODULE

CUSTOMER	
MODEL	**SCT040013-V01**
CUSTOMER APPROVED	

APPROVED BY	CHECKED BY	ORGANIZED BY
Zane Lau	Lr.Yin	Wf.Luo

RECORDS OF REVISIONS

Version	Content	Date
A0	First Issue	2020-04-29

CONTENTS

1. General Description

This Module SCT040013-V01 is TFT Liquid Crystal Display Module. This specification covers the delivery requirements for the liquid crystal display module delivered by quality to Customer.

1.1. Mechanical & Display Specifications

Item	Standard value	Unit
LCD Size	3.97	inch
Dot Matrix	480(RGB) ×800	pixel
Module Size	59.14 ×97.95 ×3.80	mm
Active Area	51.84 ×86.40	mm
Dot Pitch	0.108 ×0.108	mm
Pixel Configuration	R.G.B. Stripe	-
Color depth	16.7M	-
Display Mode	Normally Black, Transmissive	-
Technology Type	a-Si	-
Viewing Direction	ALL	-
Gray Scale Inversion Direction	ALL	-
Driver IC	ILI9806E	-
Interface	SPI + RGB	-
LED Numbers	8 LEDs	-
Weight	TBD	g

1.2. Interface Pin

Pin No.	Symbol	Type	Description
1	GND	P	Ground
2	VCC	P	Power supply for system
3	VDDI	P	Power supply for I/O
4	TE	O	Tearing effect output
5	SDO	O	Serial data output
6	SDI	I	Serial data input
7	SCL	I	Serial interface clock
8	CS	I	Chip Select signal
9	RESET	I	Chip reset signal
10-17	R7 – R0	I	Data bus for Red
18-25	G7 – G0	I	Data bus for Green
26-33	B7 – B0	I	Data bus for Blue
34	GND	P	Ground
35	DE	I	Data enable input
36	PCLK	I	Pixel clock input
37	HSYNC	I	Horizontal sync input
38	VSYNC	I	Vertical sync input
39	PWM	O	PWM frequency output for LED driver control (open when not in use)
40	GND	P	Ground
41	LEDK	P	LED driving cathode
42	LEDA	P	LED driving anode
43	NC	-	No connection
44	CTP-GND	P	Ground for CTP
45	CTP-VDD	P	Power supply for CTP
46	CTP-GND	P	Ground for CTP
47	CTP-RESET	I	Chip reset signal for CTP
48	CTP-INT	O	CTP interrupt output
49	CTP-SCL	I	CTP I2C clock
50	CTP-SDA	I/O	CTP I2C data
51	CTP-GND	P	Ground for CTP

Note1: TYPE definition: I-----Input O---Output P----Power/Ground

2. Electrical Characteristics

2.1. Absolute Maximum Rating

Item	Symbol	Min.	Max.	Unit	Remark
Power Supply Voltage (Analog)	VCC	-0.3	4.6	V	
Power Supply Voltage (IO)	VDDI	-0.3	4.6	V	
Power Supply Voltage	CTP-VDD	-0.3	4.2	V	
Input Signal Voltage	V_{IN}	-0.3	4.6	V	Note 1
Operating Temperature	T_{OPR}	-20	+70	℃	Ambient
Storage Temperature	T_{STG}	-30	+80	℃	Ambient

Note1: VIN represent IO

2.2. DC Electrical Characteristics

2.2.1. Driving TFT LCD Panel

GND=0V, Ta=25℃

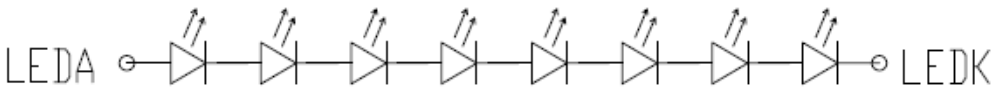
Item	Symbol	Min.	Typ.	Max.	Unit	Remark
Analog Operating Voltage	VCC	2.5	2.8	3.6	V	
Logic Operating Voltage	VDDI	1.65	1.8	3.6	V	
CTP Operating Voltage	CTP-VDD	2.8	2.8	3.3	V	
Logic High level input voltage	V_{IH}	0.7VDDI	-	VDDI	V	
Logic Low level input voltage	V_{IL}	-0.3	-	0.3VDDI	V	
Logic High level output voltage	V_{OH}	0.8VDDI	-	VDDI	V	I_{OH}=-1.0mA
Logic Low level output voltage	V_{OL}	0	-	0.2VDDI	V	I_{OL}=1.0mA
Power Consumption	I_{CC}	-	TBD	-	mA	

2.2.2. Driving Backlight

Ta=25℃

Item	Symbol	Min.	Typ.	Max.	Unit	Remark
Forward Current	I_F	-	20	20	mA	Note1
Forward Current Voltage	V_F	22	-	28	V	
Operating Life Time	-	10000			hrs	

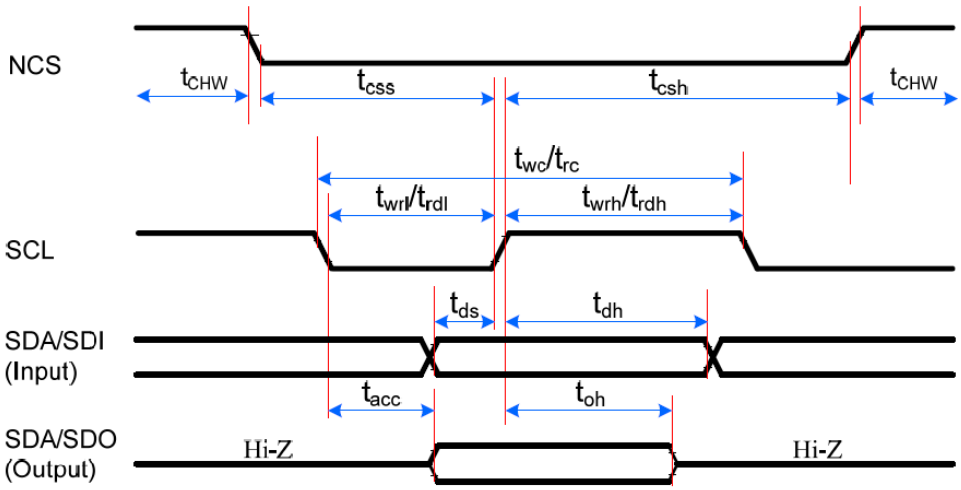
Note 1: The figure below shows the connection of backlight LED.



Note 2: One LED: I_F =20mA.

2.3. AC Electrical Characteristics

2.3.1. Serial Interface Timing Characteristics (3-line SPI)

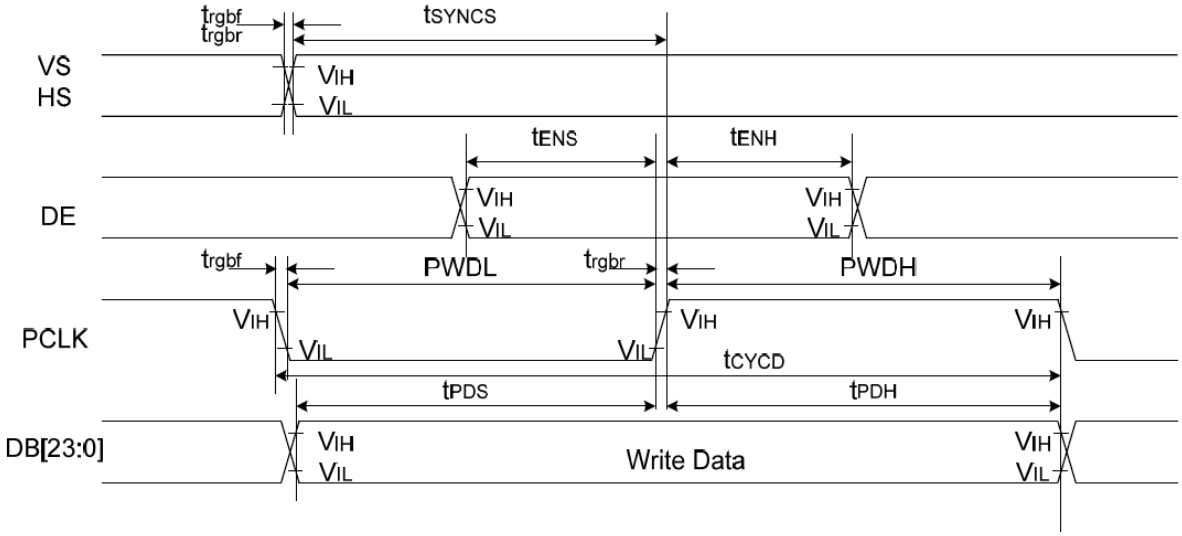


Signal	Symbol	Parameter	min	max	Unit	Description
CSX	tcss	Chip select time (Write)	15	-	ns	
	tcsh	Chip select hold time (Read)	15	-	ns	
	tCHW	CS "H" pulse width	40	-	ns	
SCL	twc	Serial clock cycle (Write)	30	-	ns	
	twrh	SCL "H" pulse width (Write)	10	-	ns	
	twrl	SCL "L" pulse width (Write)	10	-	ns	
	trc	Serial clock cycle (Read)	150	-	ns	
	trdh	SCL "H" pulse width (Read)	60	-	ns	
	trdl	SCL "L" pulse width (Read)	60	-	ns	
SDA/SDO (Output)	tacc	Access time (Read)	10	100	ns	For maximum CL=30pF
	toh	Output disable time (Read)	15	100	ns	For minimum CL=8pF
SDA/SDI (Input)	tds	Data setup time (Write)	10	-	ns	
	tdh	Data hold time (Write)	10	-	ns	

Note:

1. Ta = -30 to 70 °C, IOVCC=1.65V to 3.6V, VCI=2.5V to 3.6V, T=10+/-0.5ns.
2. Does not include signal rise and fall times.

2.3.2. Parallel 24/18/16-bit RGB Interface Timing Characteristics



Signal	Symbol	Parameter	min	max	Unit	Description
VS/ HS	t_{SYNCS}	VS/HS setup time	5	-	ns	24/18/16-bit bus RGB interface mode
	t_{SYNCH}	VS/HS hold time	5	-	ns	
DE	t_{ENS}	DE setup time	5	-	ns	
	t_{ENH}	DE hold time	5	-	ns	
DB[23:0]	t_{POS}	Data setup time	5	-	ns	
	t_{PDH}	Data hold time	5	-	ns	
PCLK	PWDH	PCLK high-level period	13	-	ns	
	PWDL	PCLK low-level period	13	-	ns	
	t_{CYCD}	PCLK cycle time	28	-	ns	
	t_{rgbr} , t_{rgbf}	PCLK,HS,VS rise/fall time	-	15	ns	

Note: Ta = -30 to 70 °C, IOVCC=1.65V to 3.6V, VCI=2.5V to 3.6V, DGND=0V

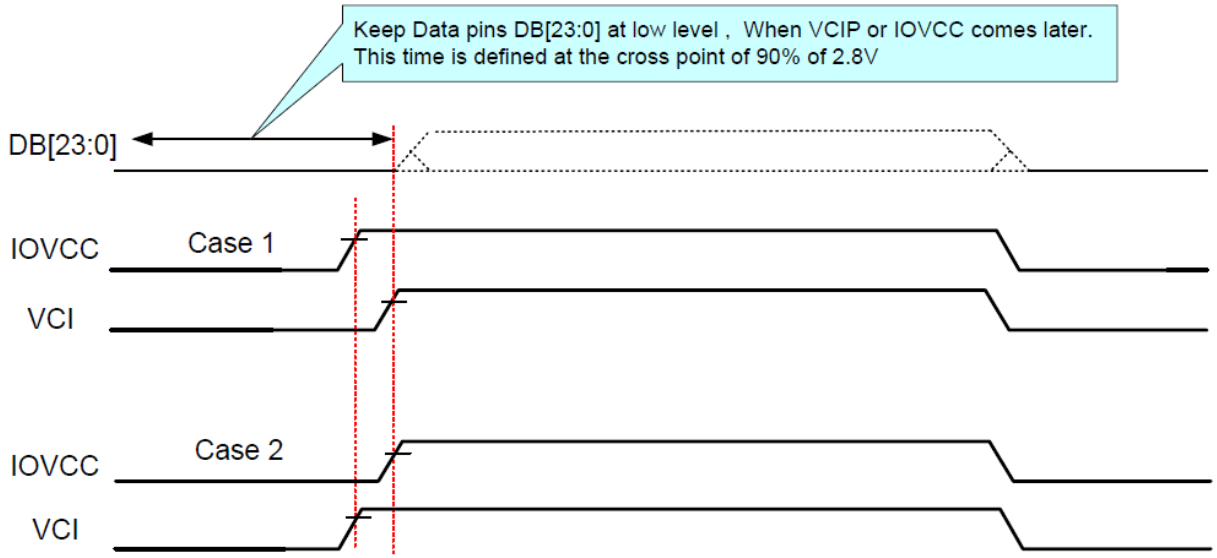
2.3.3. Power On/Off Sequence

IOVCC and VCI can be applied (or powered down) in any order. During the power off sequences, if LCD is in the Sleep Out mode, VCI and IOVCC must be powered down with minimum 120msec, and if LCD is in the Sleep In mode, VCI and IOVCC can be powered down with minimum 0msec after RESX has been released.

CSX can be applied at any timing or can be permanently grounded. RESX has priority over CSX.

Note:

1. There will be no damage to ILI9806E if the power sequences are not met.
2. There will be no abnormal visible effects on the display panel during the Power On/Off Sequences.
3. There will be no abnormal visible effects on the display between end of Power On Sequence and before receiving Sleep Out command. Also between receiving Sleep In command and Power Off Sequence.
4. If RESX line is not held stable by host during Power On Sequence as defined in Sections 7.1 and 7.2, then it will be necessary to apply a Hardware Reset (RESX) after Host Power On Sequence is complete to ensure correct operation. Otherwise function is not guaranteed.
5. Keep data pins DB[23:0] at low level, when VCIP or IOVCC comes later



3. Optical Characteristics

Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Remark
Viewing angle		θT	CR≥10	70	80	-	degree	Note5
		θB		70	80	-		
		θL		70	80	-		
		θR		70	80	-		
Contrast Ratio		CR	θ=0 ° optimal	600	800	-	-	Note3
Response Time		T_R	Ta=25℃	-	10	15	ms	Note2
		T_F		-	15	25		
Color Chromaticity	White	x	θ=0°	-0.05	0.302	+0.05	-	Note6
		y			0.329			
	Red	x			0.656			
		y			0.327			
	Green	x			0.288			
		y			0.593			
	Blue	x			0.138			
		y			0.108			
Uniformity		U	θ=0°	70	80	-	%	Note7
Luminance		L	I_F=Max.	-	190	-	cd/m^2	Note8

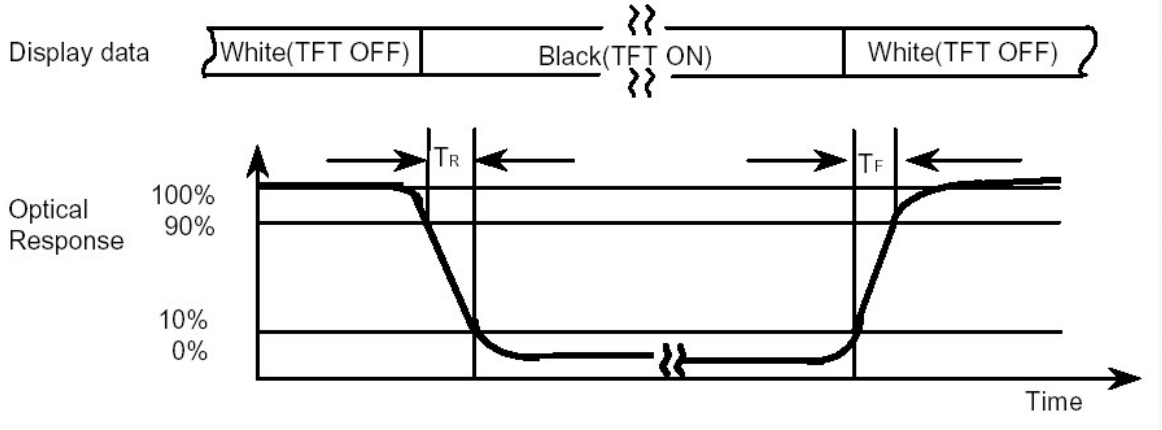
Note:

1. Test equipment setup

After stabilizing and leaving the panel alone at a given temperature for 30 minutes, the measurement should be executed. Measurement should be executed in a stable, windless, and dark room. Optical specifications are measured by Topcon BM-5A with a viewing angle of 1 °at a distance of 50cm and normal direction.

2. Definition of response time: TR and TF

The figure below is the output signal of the photo detector.

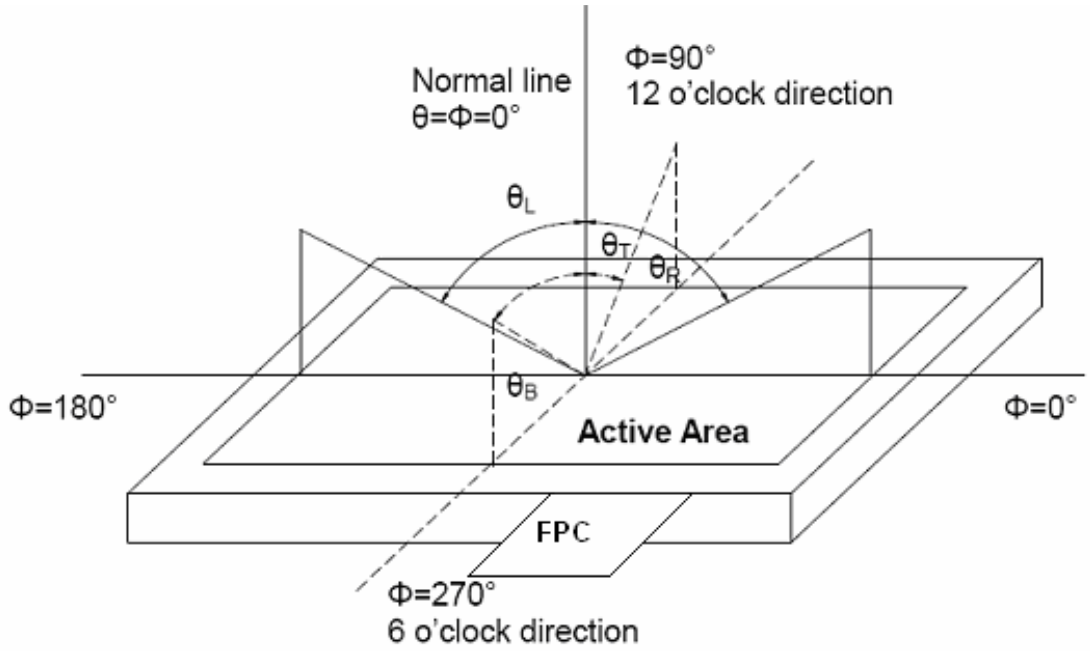


3.Definition of contrast ratio

$$CR = \frac{\text{Luminance with all pixel white}}{\text{Luminance with all pixel black}}$$

4.The 100% transmission is defined as the transmission of LCD panel when all the input terminals of module are electrically opened.

5. Definition of viewing angle:



6. Definition of color chromaticity (CIE1931)

Color coordinates measured at center point of LCD.

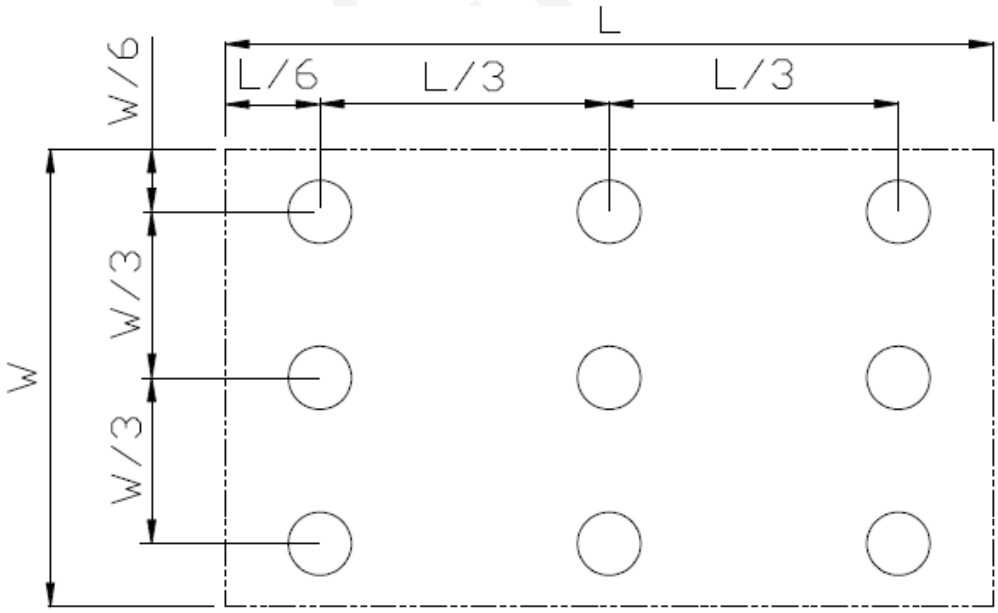
7. Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (Refer Fig.). Every measuring point is placed at the center of each measuring area.

Luminance Uniformity(U) = Lmin / Lmax

L-------Active area length

W----- Active area width



Lmax: The measured maximum luminance of all measurement position.

Lmin: The measured minimum luminance of all measurement position.

8. Definition of Luminance:

Measure the luminance of white state at center point.

4. Reliability

4.1. Reliability Condition

No.	Item	Condition	Remark
1	High temperature Operating	70℃, 240hrs	Finish product (With polarizer)
2	Low temperature Operating	-20℃, 240hrs	Finish product (With polarizer)
3	High temperature Storage	80℃, 240hrs	Finish product (With polarizer)
4	Low temperature Storage	-30℃, 240hrs	Finish product (With polarizer)
5	High temperature & Humidity Storage	80℃, 90%RH, 240hrs	Finish product (With polarizer)
6	Thermal Shock Storage (No operation)	-30℃, 30min. <=> 80℃,30min. 100 Cycles	Finish product (With polarizer)
7	ESD Test	Voltage: +8KV R:330Ω, C:150pF Air discharge, 10 times	Finish product (With polarizer)
8	Vibration Test	0.015G*G/Hz from 5-200HZ, -6dB/Octave from 200-500HZ 2 hours for each direction of X. Y. Z. (6 hours for total)	Finish product (With polarizer)
9	Drop Test	Packed, 60cm free fall 1 corner, 3 edges, 6 surfaces	Finish product (With polarizer)

*One single product test for only one item.

* Judgment after test: keep in room temperature for more than 2 hours.

- Current consumption < 2 times of initial value
- Contrast > 1/2 initial value
- Function: work normally

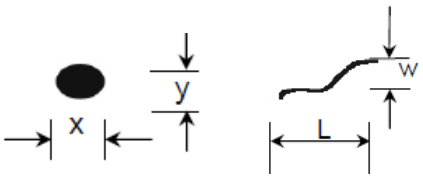
4.2. Inspection plan

Class	Item	Judgment	Class
Packing & Indicate	1.Outside and inside package	"Model no.", "lot no." and" quantity" should indicate on the package.	Minor
	2.Model mixed and quantity	Other model mixed ..... rejected. Quantity short or over ..... rejected.	Critical
	3.Product indication	"Model no." should indicate on the product	Major
Assembly	4.Dimension,LCD glass scratch and scribe defect	According to specification or drawing	Major
Appearance	5.Viewing area	Polarizer edge or LCD's sealing line is visible in the viewing area ..... rejected	Minor
	6.Blemish,black spot, white spot in the LCD and LCD glass cracks	According to standard of visual inspection (inside viewing area)	Minor
	7.Blemish,black spot White spot and scratch on the polarizer	According to standard of visual inspection (inside viewing area)	Minor
	8.Bubble in polarizer	According to standard of visual inspection (inside viewing area)	Minor
	9.LCD's rainbow color	Strong deviation color (or Newton ring) of LCD ..... rejected. Or according to limited sample (if needed, and inside viewing area)	Minor
	10.FPC	Burned area or wrong part number is on FPC. The symbol, character, and mark of FPC are unidentifiable. The stripped solder mask, A>1.0mm. 0.3mm < stripped solder mask or visible circuit, A<1.0mm,and the number is ≧4 pieces. Particle between circuits in solder mask. Circuit is peeled off or cracked. Any circuit risen or exposed. 0.2mm< Area of solder ball, A is ≦0.4mm,the number of solder ball is ≧3 pieces. The magnitude of solder ball, A is > 0.4mm.	Minor
Electrical	11.Electrical and optical characteristics (contrast, VOP, chromaticity etc.)	According to standard of visual inspection (inside viewing area)	Critical
	12.Missing pattern	Missing dot, line, character ..... rejected	Critical
	13.Short circuit, wrong pattern display	Non display, wrong pattern display, current consumption out of specification ..... rejected	Critical
	14.Pin hole, pattern deformity	According to standard of visual inspection	Minor
	15.Black spot, white spot, black line, white line, slant line, background uneven, color uneven	Strong deviation color ..... rejected Or according to limited sample full off screen (all black) ..... disregards	Minor
	16.Stick image (retention image)	Fixed test picture within two hours ..... rejected	Minor

4.3. Standard of visual inspection

<table>
<tr><th>Class</th><th>Item</th><th>Judgment</th></tr>
<tr><td>Minor</td><td>Blemish, black spot, white spot in the LCD.

Blemish, black spot, white spot and scratch on the polarizer.

Round type Line type</td><td>(A) Round type Unit: mm
<table><tr><td>Diameter (mm)</td><td>Acceptable Quantity</td></tr><tr><td>0.25 < A</td><td>0</td></tr></table>Note: A = (x + y)/2 (mm)

(B) Line type Unit: mm
<table><tr><td>Length</td><td>Width</td><td>Acceptable Quantity</td></tr><tr><td>-</td><td>W≦0.03</td><td>Acceptable</td></tr><tr><td>L<5</td><td>0.03<W≦0.07</td><td>3</td></tr><tr><td>L<5</td><td>0.03<W≦0.07</td><td>1</td></tr><tr><td>-</td><td>0.07<W</td><td>Follow round type</td></tr></table></td></tr>
<tr><td>Minor</td><td>Bubble in polarizer</td><td>Unit: mm
<table><tr><td>Diameter (mm)</td><td>Acceptable Quantity</td></tr><tr><td>A < 0.3</td><td>Acceptable</td></tr><tr><td>0.3 < A < 0.5</td><td>1</td></tr><tr><td>0.5 < A</td><td>0</td></tr></table>Note: A = (Length +Width)/2 (mm)</td></tr>
<tr><td>Minor</td><td>Pin hole, Pattern deformity</td><td>Unit: mm
<table><tr><td>Diameter (mm)</td><td>Acceptable Quantity</td></tr><tr><td>0.4 < Φ</td><td>0</td></tr></table>Note: Φ = (a +b)/2 (mm)</td></tr>
</table>

5. Precautions

5.1. Handling Precautions

(1) Protect the panel from static, it may cause damage to the CMOS Gate Array IC.

(2) Use fingerstalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.

(3) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, legs or clothes, it must be washed away thoroughly with soap.

(4) The desirable cleaners are water, IPA (Isopropyl Alcohol) or Hexane. Don't use Ketone type materials (ex. Acetone), Ethyl alcohol, Toluene, Ethyl acid or Methyl chloride. It might permanent damage to the polarizer due to chemical reaction.

(5) Pins of I/F connector shall not be touched directly with bare hands.

(6) Refrain from strong mechanical shock and / or any force to the panel. In addition to damage, this may cause improper operation or damage to the panel.

(7) Note that polarizers are very fragile and could be easily damaged. Do not press or scratch the surface harder than a B pencil lead.

(8) Wipe off water droplets or oil immediately. If you leave the droplets for a long time, staining and discoloration may occur.

(9) If the surface of the polarizer is dirty, clean it using some absorbent cotton or soft cloth.

5.2. Storage Precautions

(1) Do not leave the panel in high temperature, and high humidity for a long time. It is highly recommended to store the panel with temperature from 0 to 35℃ and relative humidity of less than 70%.

(2) The panel shall be stored in a dark place. It is prohibited to apply sunlight or fluorescent light during the store.

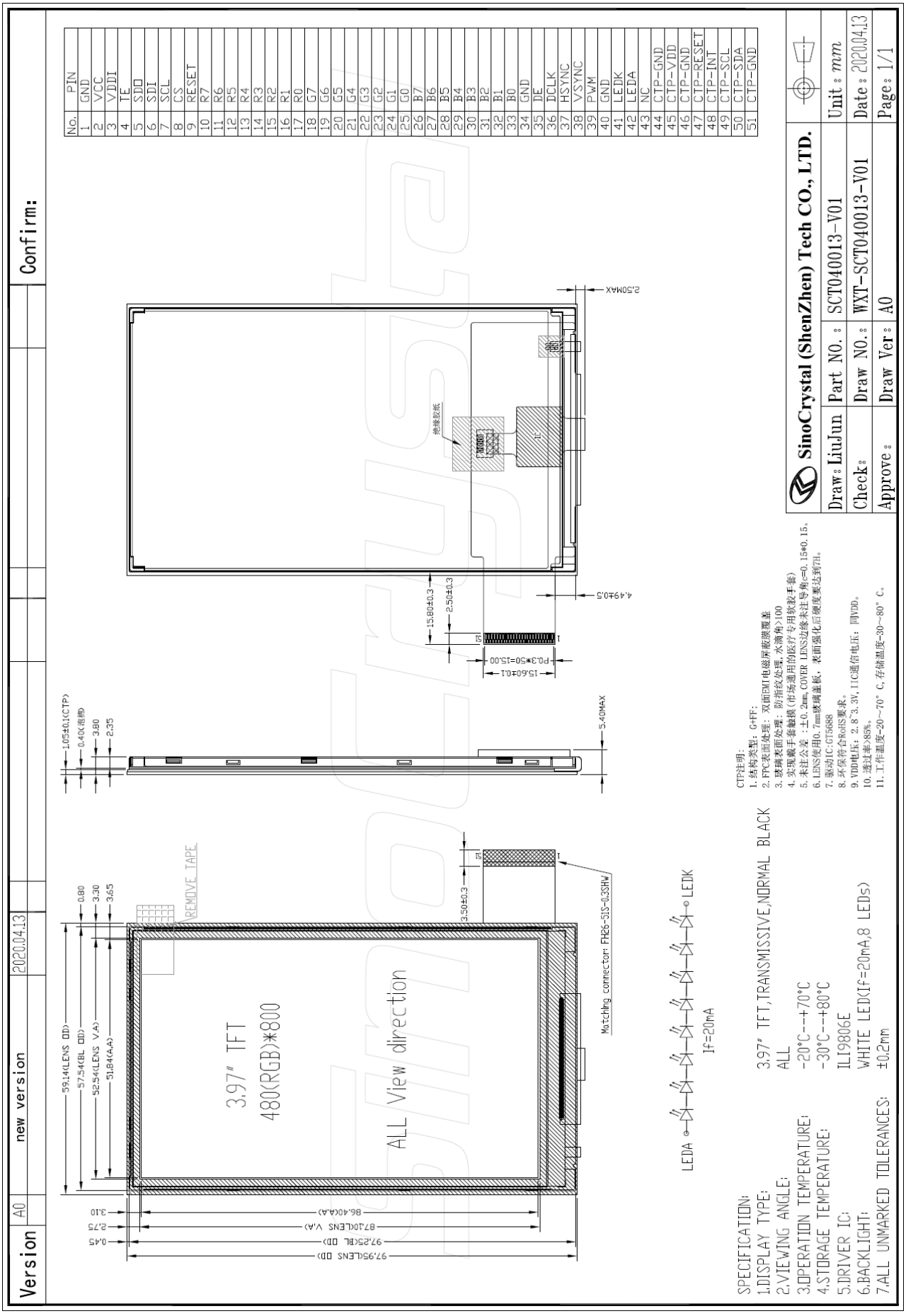
5.3. Operation Precautions

(1) The LCD shall be operated within the limits specified. Operation at values outside of these limits may shorten life, and/or harm display images.

(2) Do not exceed the absolute maximum rating value. (the supply voltage variation, Input voltage variation in part contents and environmental temperature and so on). Otherwise the panel may be damaged.

(3) If the panel displays the same pattern continuously for a long period of time, it can be the situation when the image" Sticks" to the screen.

6. Outline Dimension



7. Packing method

7.1. Packing Quantity

TBD.

7.2. Flowing chart

TBD.